

10057-1-1004

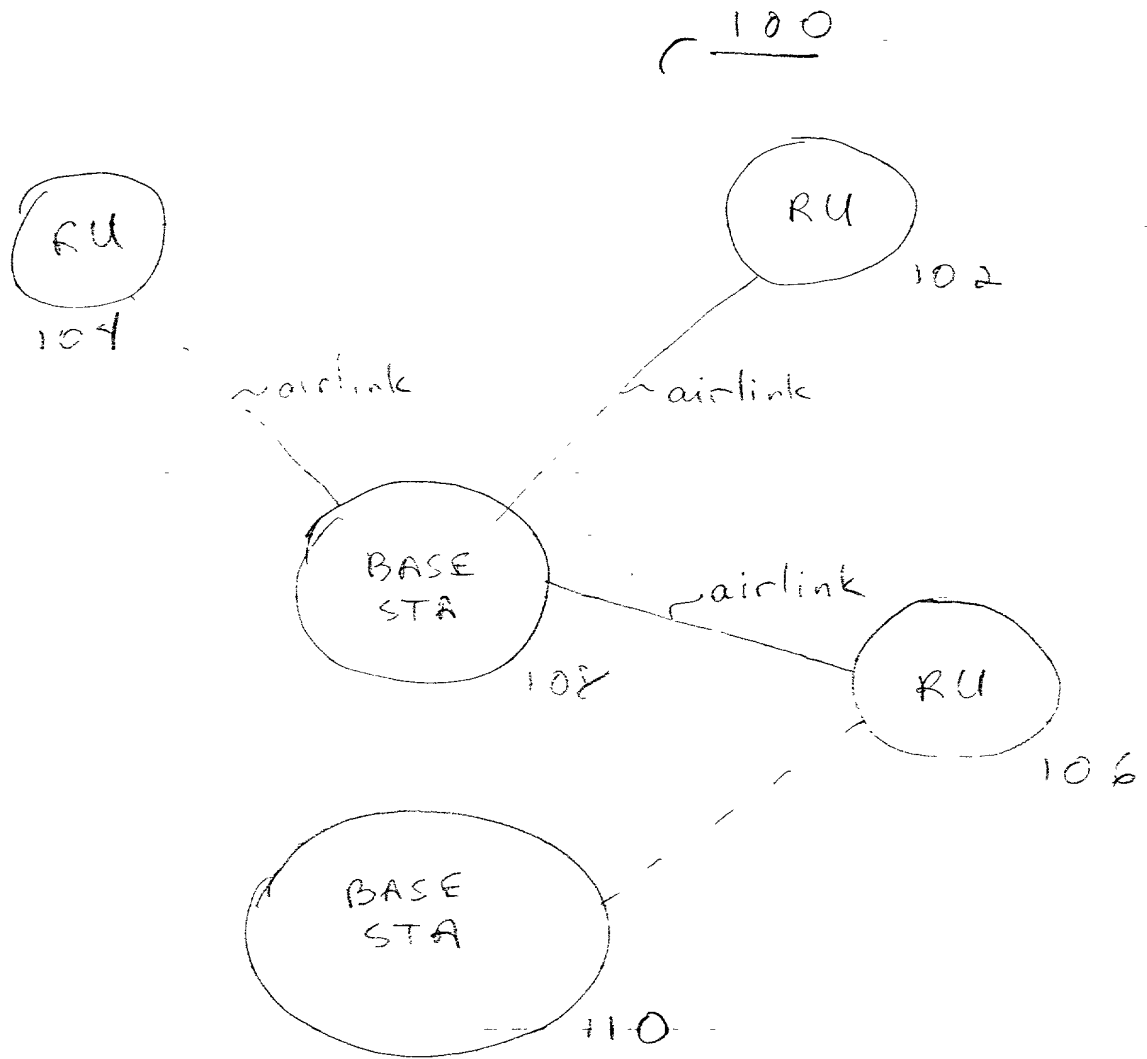


FIG. 1

Hand-drawn block diagram of a computer system. The components are arranged as follows:

- Telephony DSP 202**: Located at the top left, connected to the bus by a vertical line.
- airlink DST 204**: Located in the center, connected to the bus by a vertical line.
- CPU 206**: Located at the bottom left, connected to the bus by a vertical line.
- memory 208**: Located at the bottom right, connected to the bus by a vertical line.
- 200**: A horizontal line at the very bottom, representing the system bus, with an upward-pointing arrow from below it.

F16. 2

Terminating Call

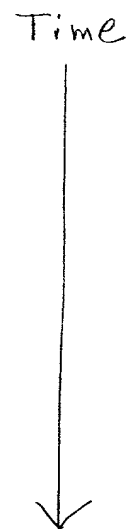
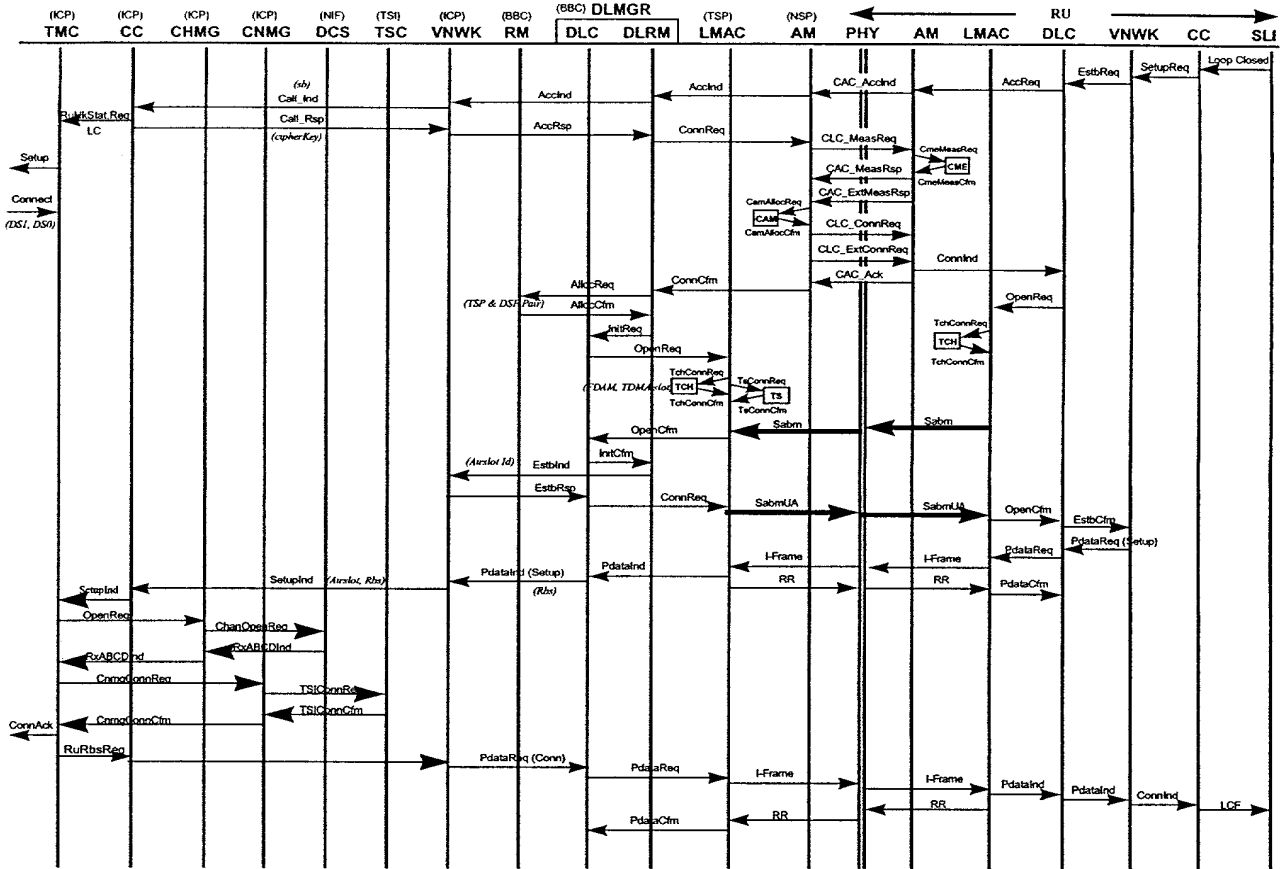
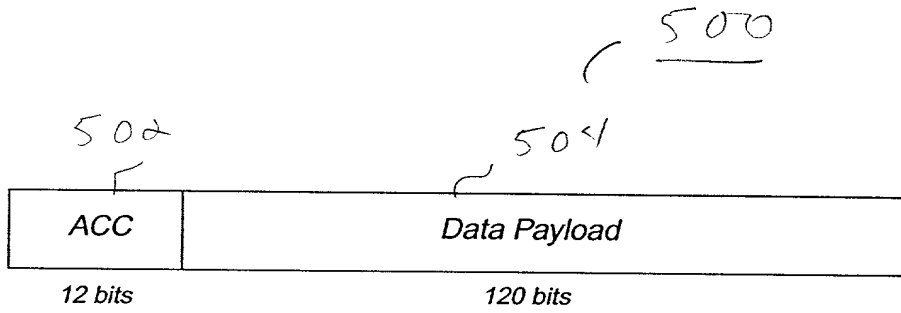


Fig. 3

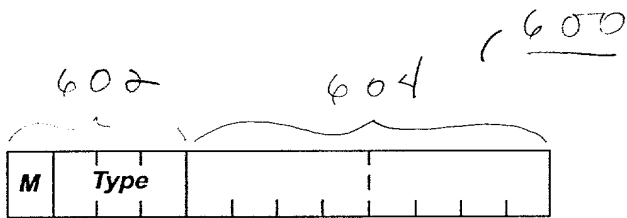
Call Startup Flow Originating Call



TOGETHER WE CAN SAVE THE WORLD



F16.5



Payload

Frame Type:

000	No Message
001	LCC Message
010	PCC Message
011	TCC Message
1xx	Reserved

More Bit - 1: Msg continued next frame

F 16. 6

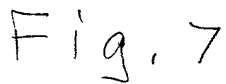
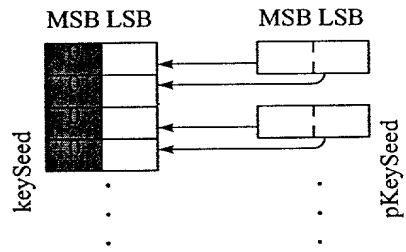
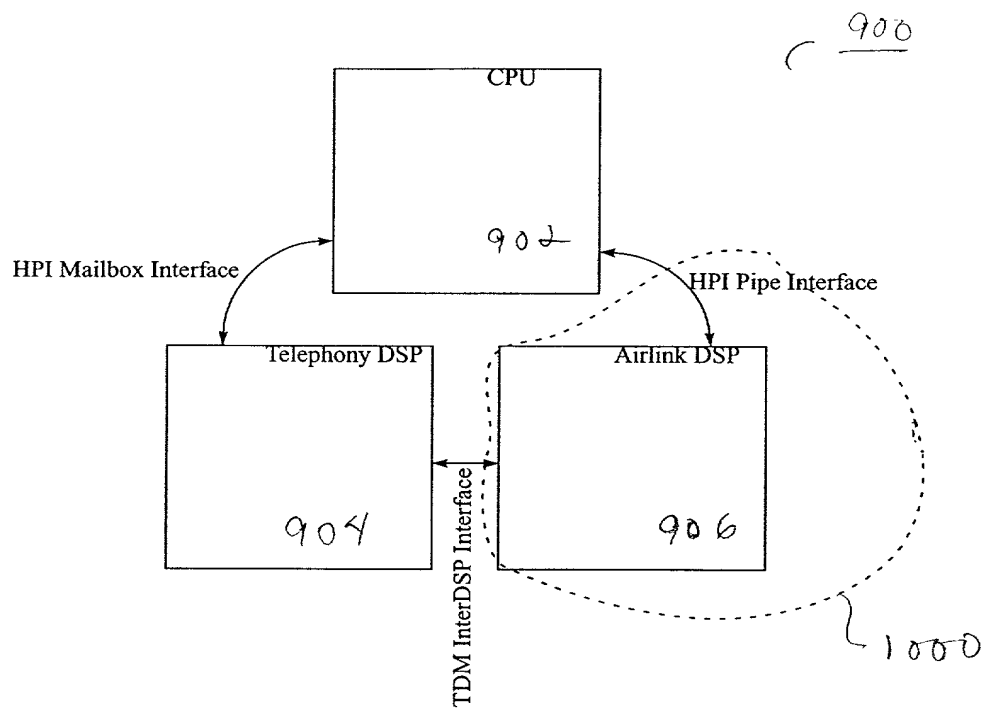
[illegible]

Fig. 7

Mapping *pKeySeed* to *keySeed*

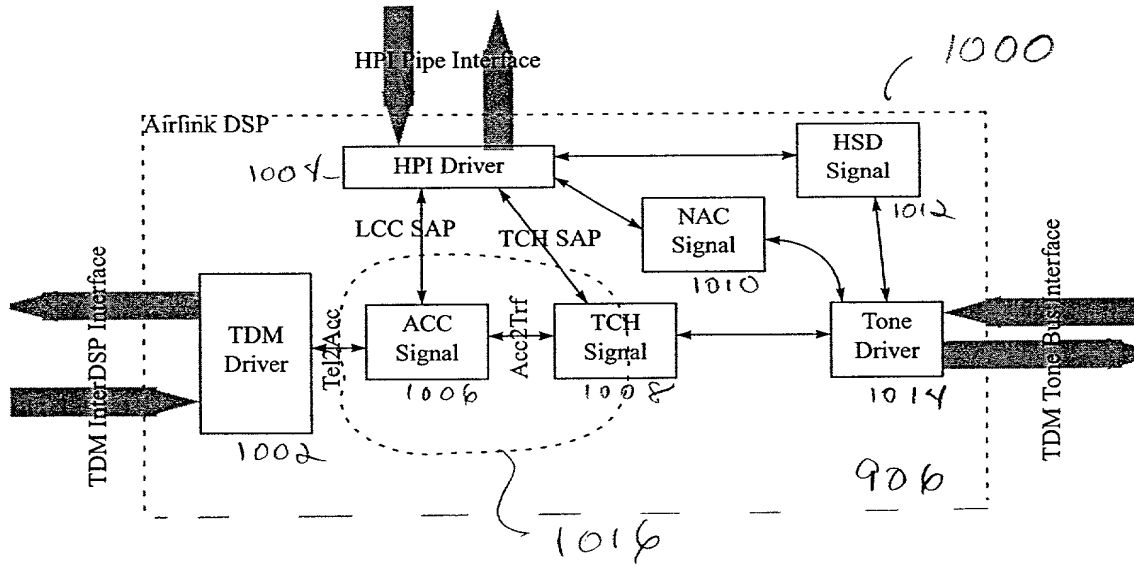


F16.8

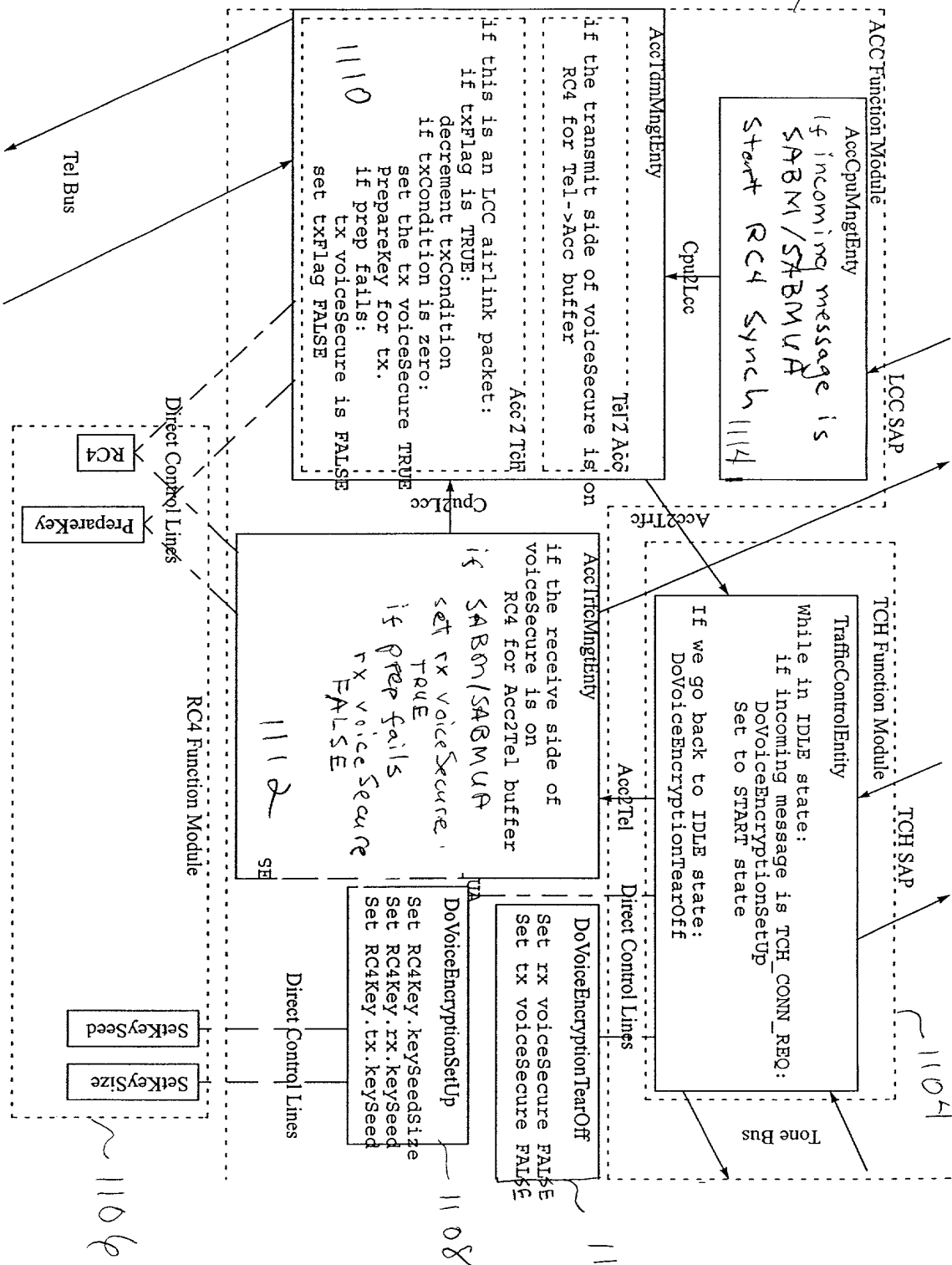


F16.9

1003 T 0000 0000 0000 0000

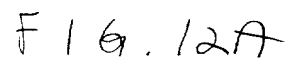


F16.10



10022573.121304

F16,11



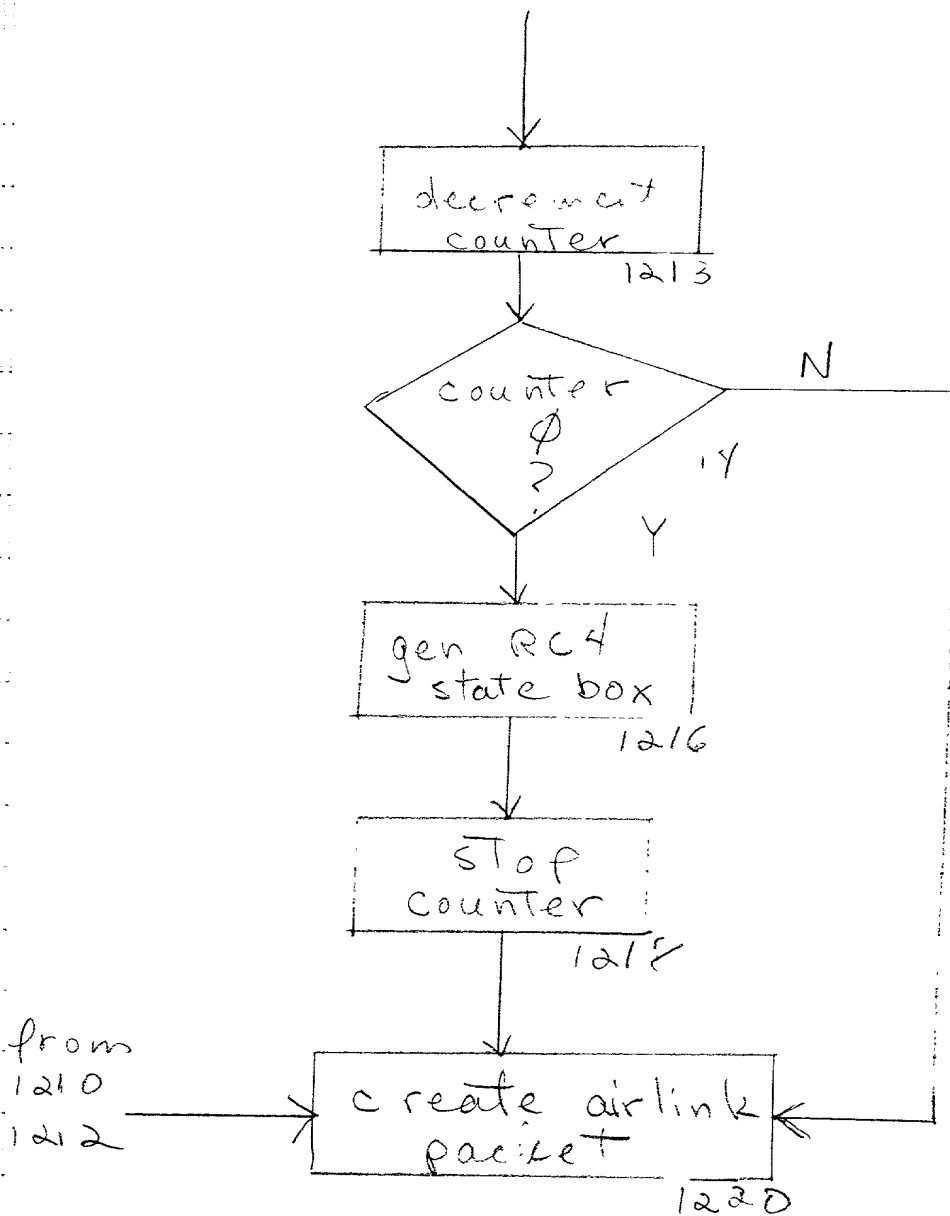
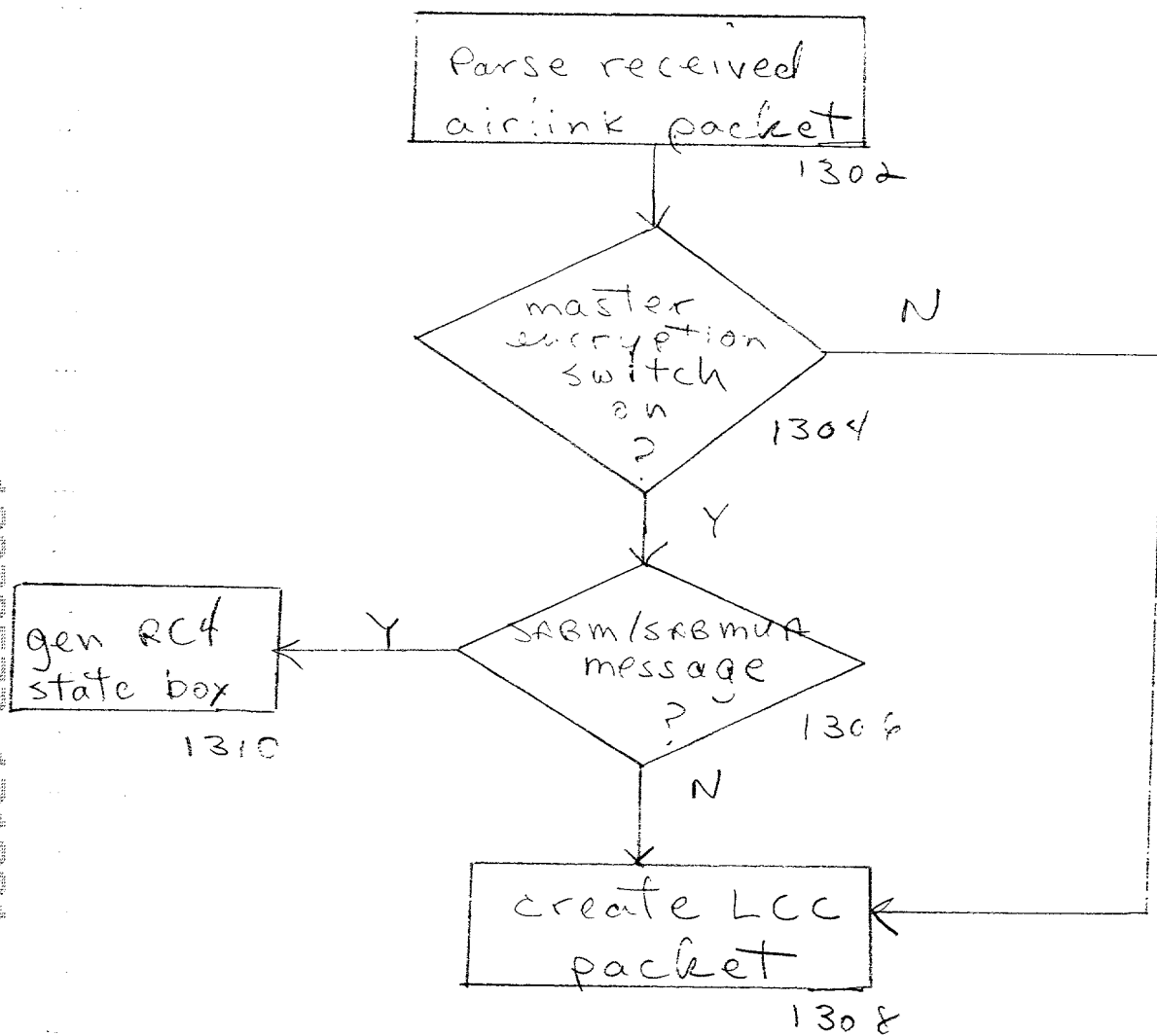
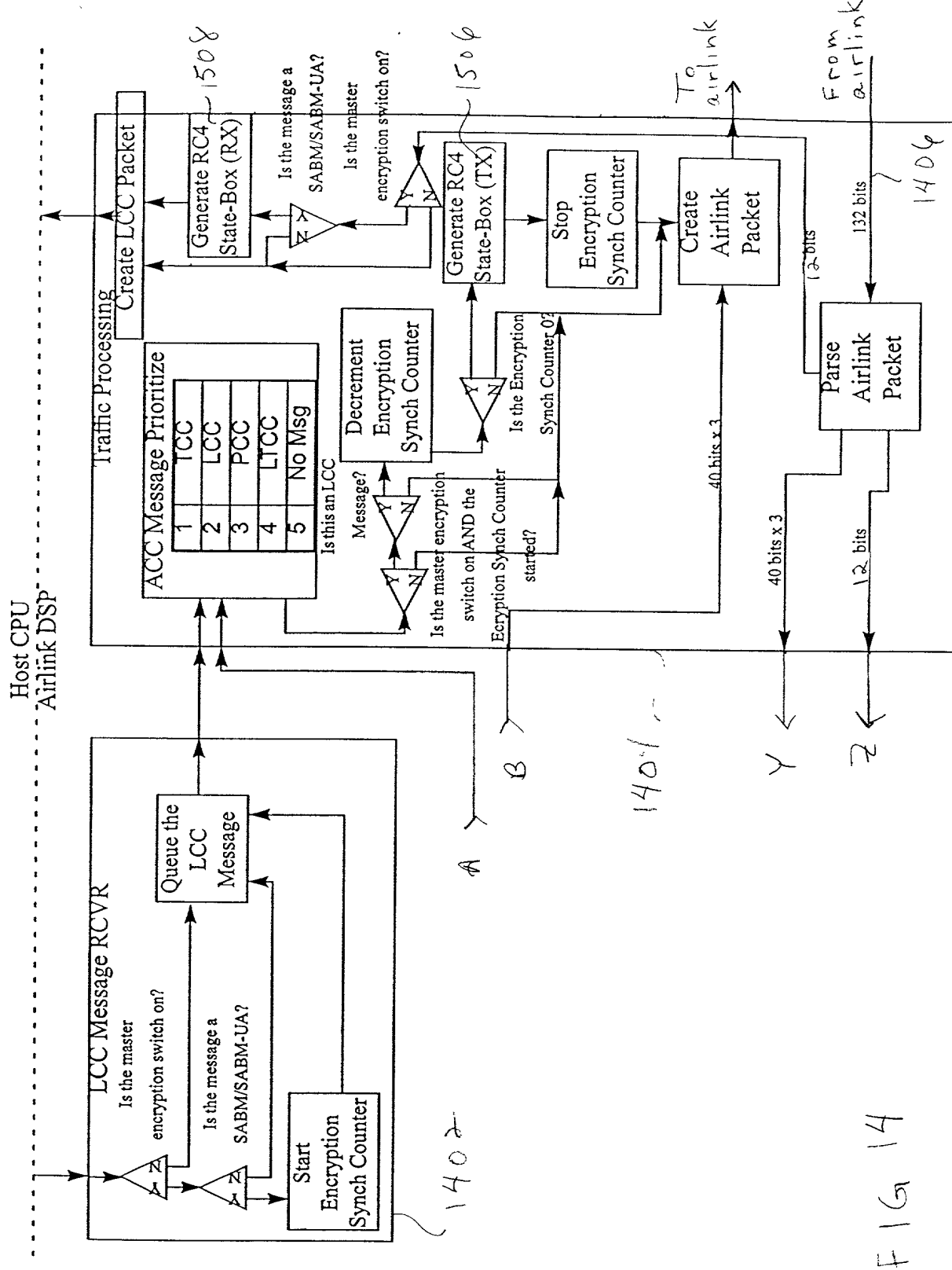
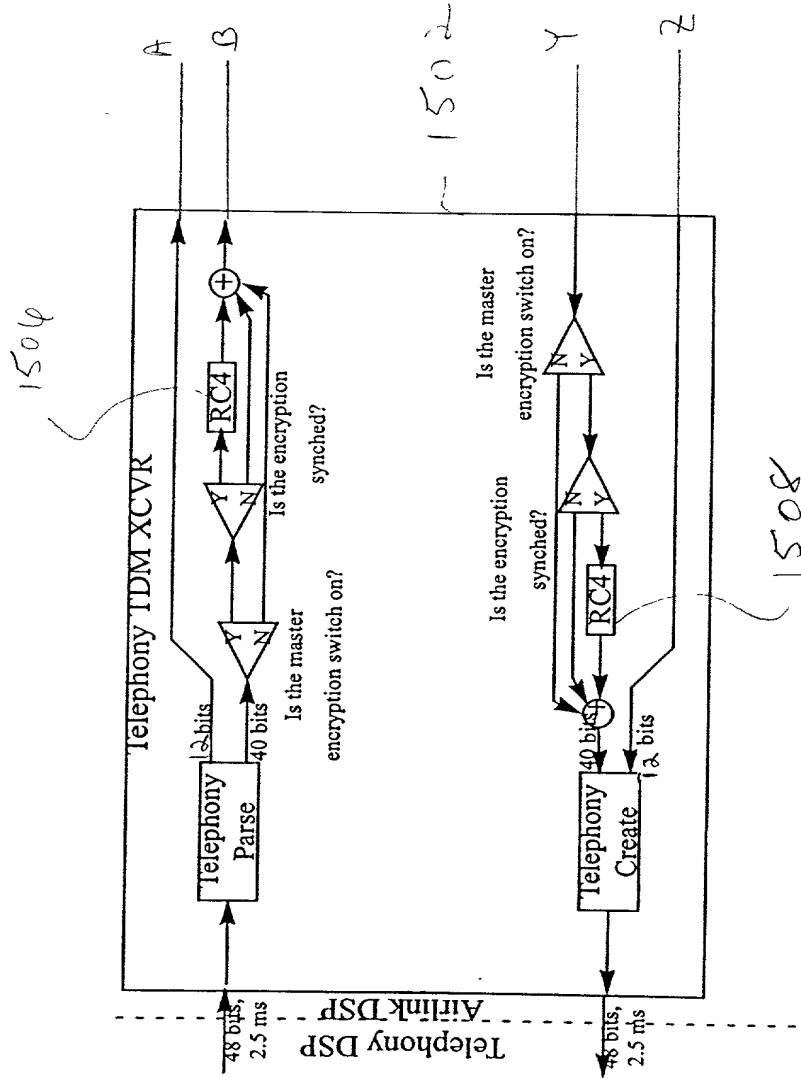


FIG. 12B



F16.13





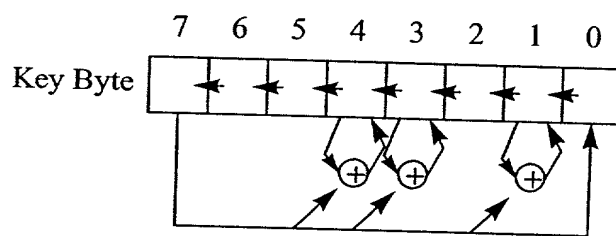
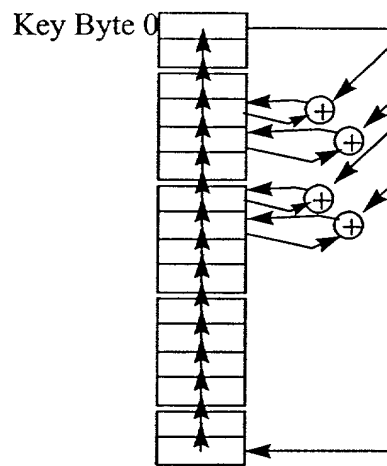


FIG. 14



F167.17